



# 650V Super-junction Power MOSFET

## Description

### 650V Super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle and pioneered. The Multi-EPI SJ MOSFET provide an extremely fast and robust body diode. Also provide an extremely low switching, communication and conduction losses device with highest robustness make especially resonant switching applications more reliable, more efficient, lighter and cooler, designed by Wuxi Unigroup Microelectronics Company.

## Features

- Ultra-fast body diode
- Very low FOM  $R_{DS(on)} \times Q_g$
- Easy to use/drive
- 100% avalanche tested
- RoHS compliant

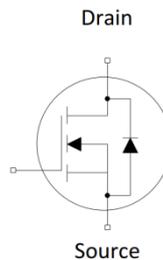
## Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- LLC Half-bridge
- Charger

TO-247



Gate



## Device Marking and Package Information

Device	Package	Marking
TPW65R044MFD	TO-247	65R044MFD

## Key Performance Parameters

Parameter	Value	Unit
$V_{DS} @ T_{j,max}$	700	V
$R_{DS(on),max}$	0.044	$\Omega$
$Q_{g,typ}$	165	nC
$I_D$	72	A
$I_{D,pulse}$	216	A
$E_{OSS} @ 400V$	19.49	$\mu J$
Body Diode $di_F/dt$	500	A/ $\mu s$
$t_{rr}$	242	ns
$Q_{rr}$	1.5	$\mu C$
$I_{rrm}$	12	A



<b>Absolute Maximum Ratings</b> $T_C = 25^\circ\text{C}$ , unless otherwise noted				
Parameter		Symbol	Value	Unit
Continuous Drain Current	$T_C = 25^\circ\text{C}$	$I_D$	72	A
	$T_C = 100^\circ\text{C}$		43.2	
Pulsed Drain Current	(note1)	$I_{D,pulse}$	216	A
Gate-Source Voltage		$V_{GSS}$	$\pm 30$	V
Single Pulse Avalanche Energy	(note2)	$E_{AS}$	2185	mJ
Repetitive Avalanche Energy	(note2)	$E_{AR}$	3.31	mJ
Avalanche Current		$I_{AR}$	13.7	A
MOSFET dv/dt Ruggedness, $V_{DS} = 0 \dots 480\text{V}$		dv/dt	50	V/ns
Power Dissipation For TO-247		$P_D$	500	W
Continuous Diode Forward Current		$I_S$	61	A
Diode Pulsed Current	(note1)	$I_{S,pulse}$	216	
Reverse Diode dv/dt	(note3)	dv/dt	50	V/ns
Maximum Diode Commutation Speed	(note3)	$di/dt$	900	A/ $\mu\text{s}$
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55~+150	$^\circ\text{C}$

<b>Thermal Resistance For TO-247</b>			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{thJC}$	0.25	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	62	



Electrical Characteristics $T_J = 25^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	650	--	--	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 650V, V_{GS} = 0V, T_J = 25^\circ\text{C}$	--	--	10	$\mu A$
		$V_{DS} = 650V, V_{GS} = 0V, T_J = 150^\circ\text{C}$	--	--	10000	
Gate-Source Leakage Current	$I_{GSS}$	$V_{GS} = \pm 30V$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	3	--	5	V
Drain-Source On-State-Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 36A$	--	0.039	0.044	$\Omega$
Gate Resistance	$R_G$	$f = 1.0\text{MHz}$ open drain	--	0.3	--	$\Omega$
<b>Dynamic Characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V,$ $V_{DS} = 100V,$ $f = 1.0\text{MHz}$	--	7837	--	$\mu F$
Output Capacitance	$C_{oss}$		--	221	--	
Reverse Transfer Capacitance	$C_{rss}$		--	13.2	--	
Total Gate Charge	$Q_g$	$V_{DD} = 520V, I_D = 50A,$ $V_{GS} = 10V$	--	165	--	nC
Gate-Source Charge	$Q_{gs}$		--	50	--	
Gate-Drain Charge	$Q_{gd}$		--	70	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 400V, I_D = 50A,$ $R_G = 25\Omega$	--	103	--	ns
Turn-on Rise Time	$t_r$		--	83	--	
Turn-off Delay Time	$t_{d(off)}$		--	543	--	
Turn-off Fall Time	$t_f$		--	93	--	
<b>Drain-Source Body Diode Characteristics</b>						
Body Diode Forward Voltage	$V_{SD}$	$T_J = 25^\circ\text{C}, I_{SD} = 36A, V_{GS} = 0V$	--	1.0	1.5	V
Reverse Recovery Time	$t_{rr}$	$V_R = 400V, I_F = 36A,$ $di_F/dt = 100A/\mu s$	--	242	--	ns
Reverse Recovery Charge	$Q_{rr}$		--	1.45	--	$\mu C$
Peak Reverse Recovery Current	$I_{rrm}$		--	12	--	A

**Notes**

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2.  $I_{AS} = 13.7A, V_{DD} = 50V, R_G = 25\Omega$ , Starting  $T_J = 25^\circ\text{C}$
3. Identical low side and high side switch with identical  $R_G$



Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Figure 1. Output Characteristics

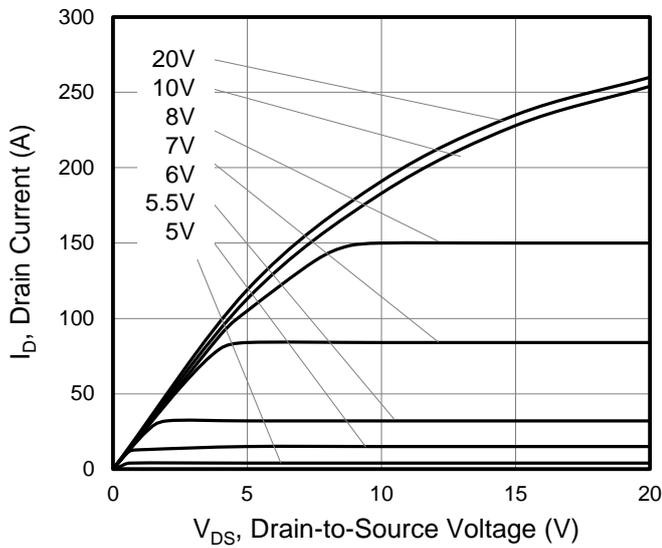


Figure 2. Transfer Characteristics

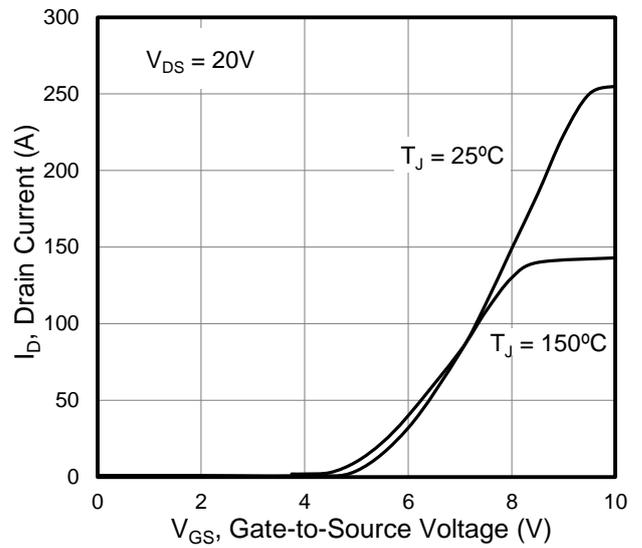


Figure 3. On-Resistance vs. Drain Current

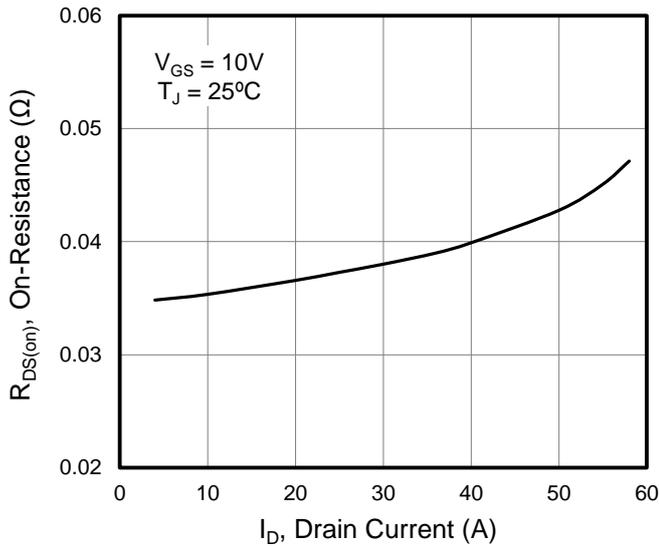


Figure 4. Capacitance

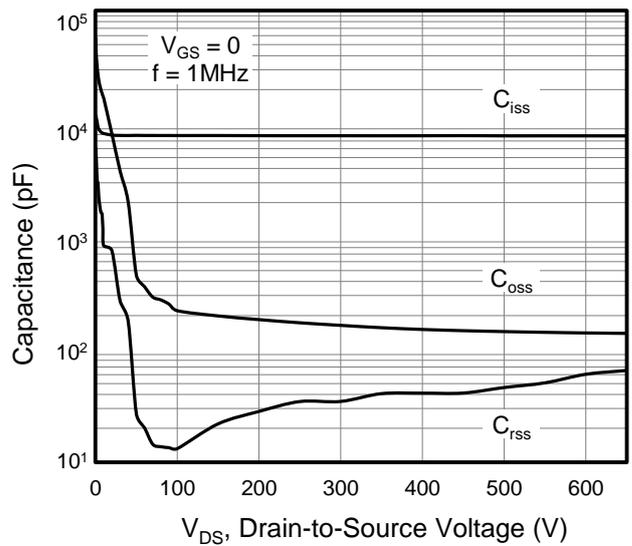


Figure 5. Gate Charge

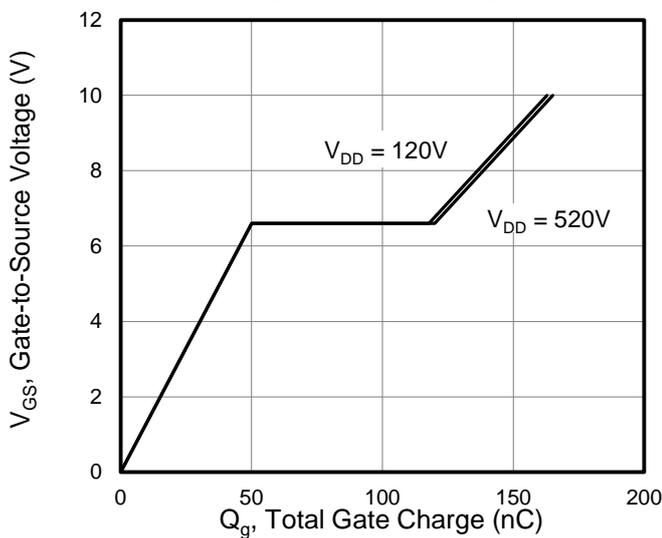
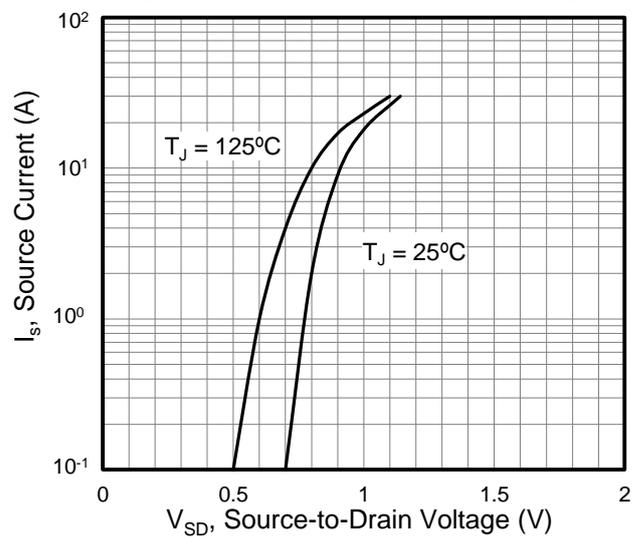


Figure 6. Body Diode Forward Voltage





Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Figure 7. On-Resistance vs. Junction Temperature

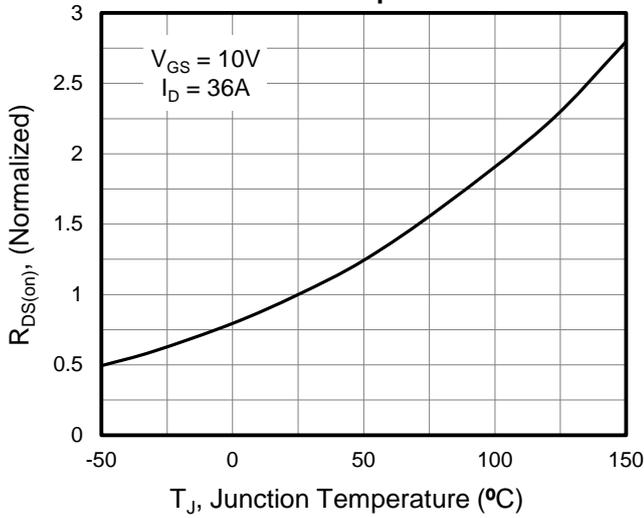


Figure 8. Breakdown voltage vs. Junction Temperature

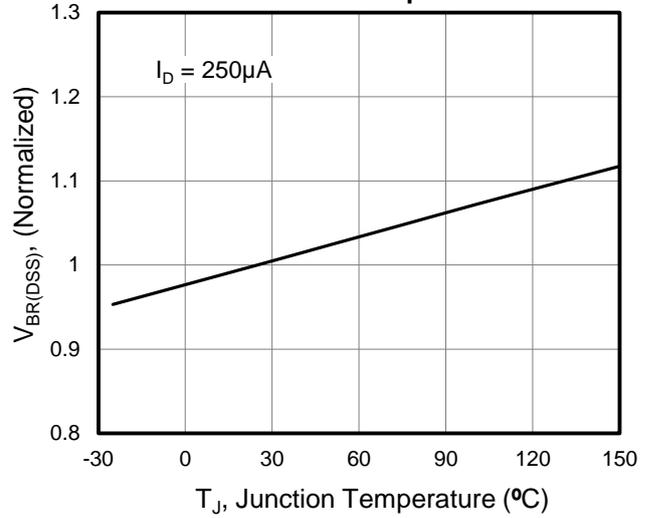


Figure 9. Transient Thermal Impedance For TO-247

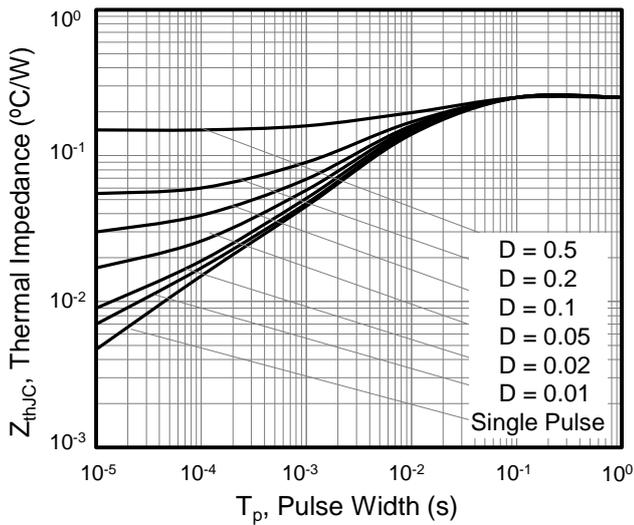


Figure 10. Safe Operation Area For TO-247

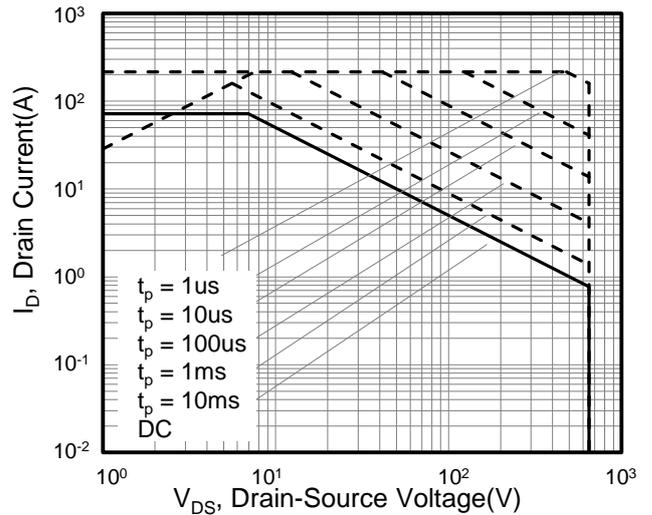


Figure 11. Typ. Coss Stored Energy

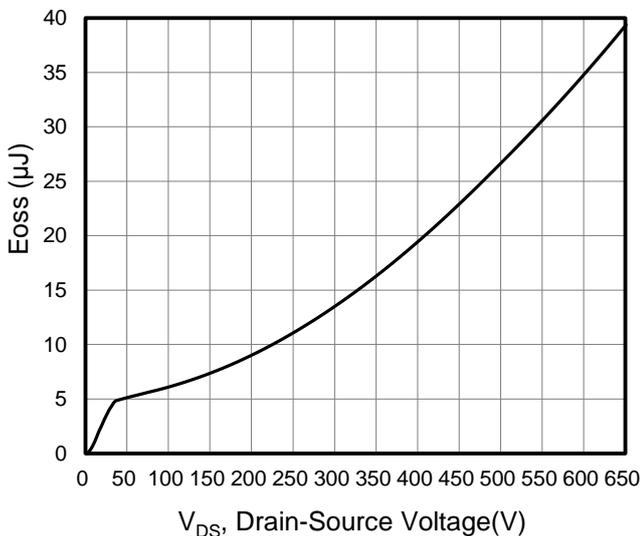




Figure A: Gate Charge Test Circuit and Waveform

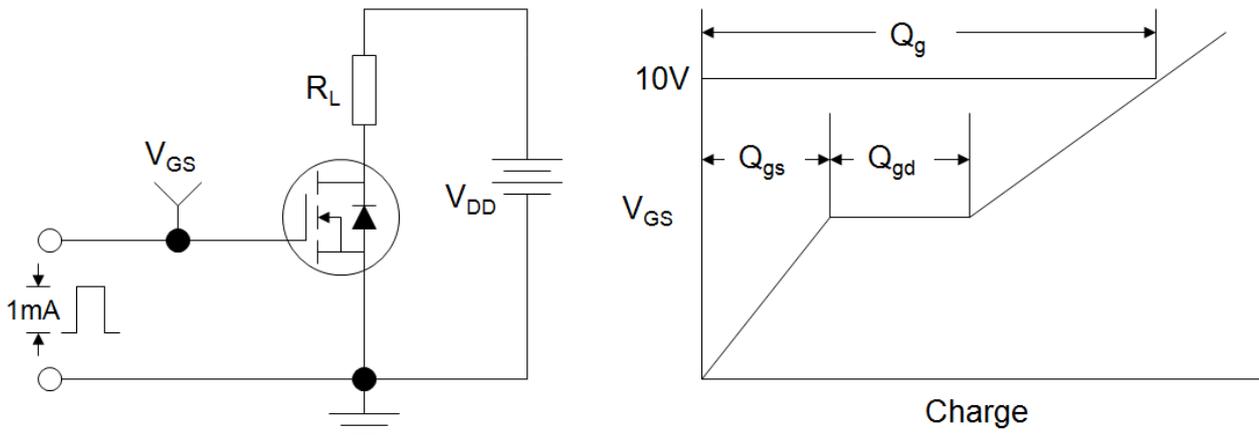


Figure B: Resistive Switching Test Circuit and Waveform

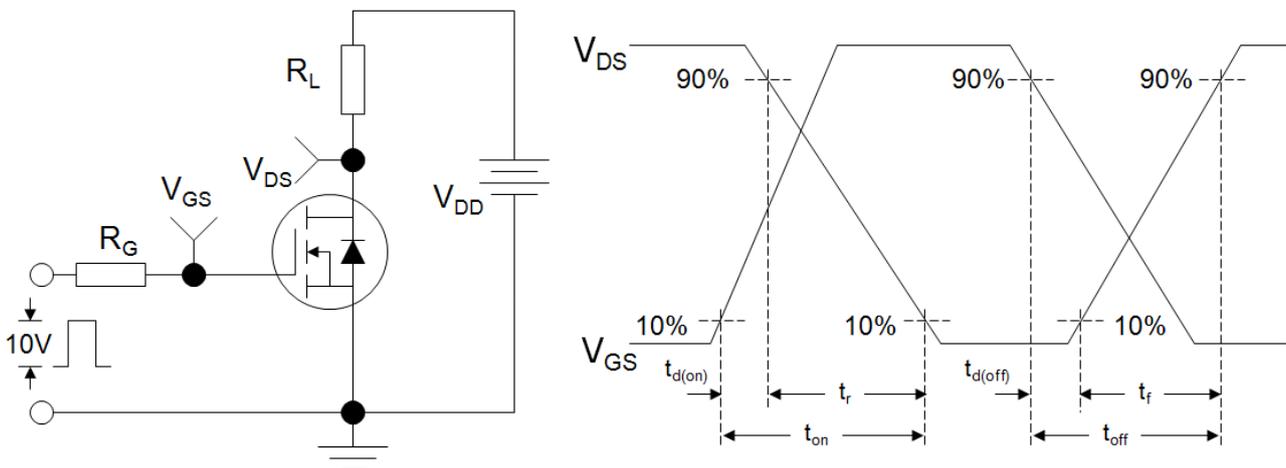
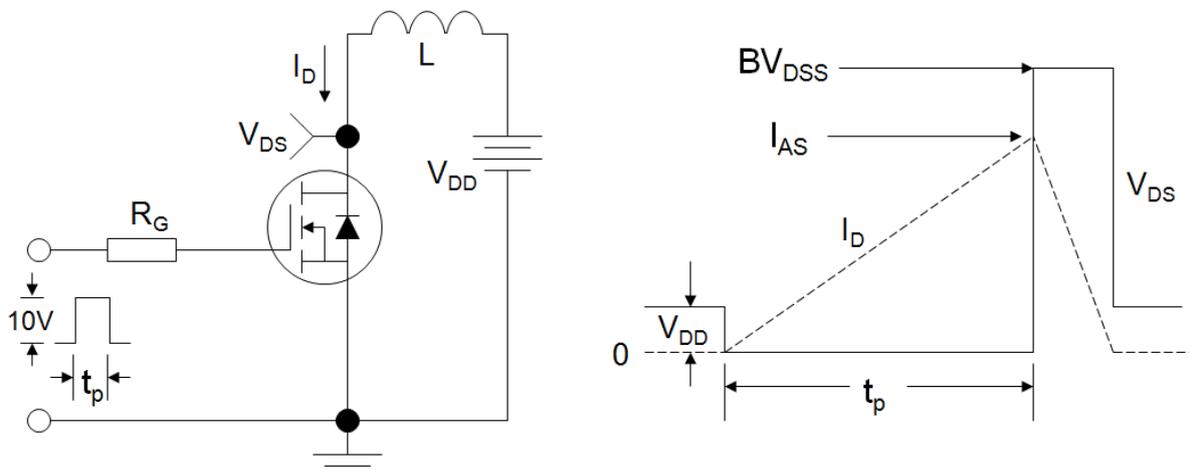
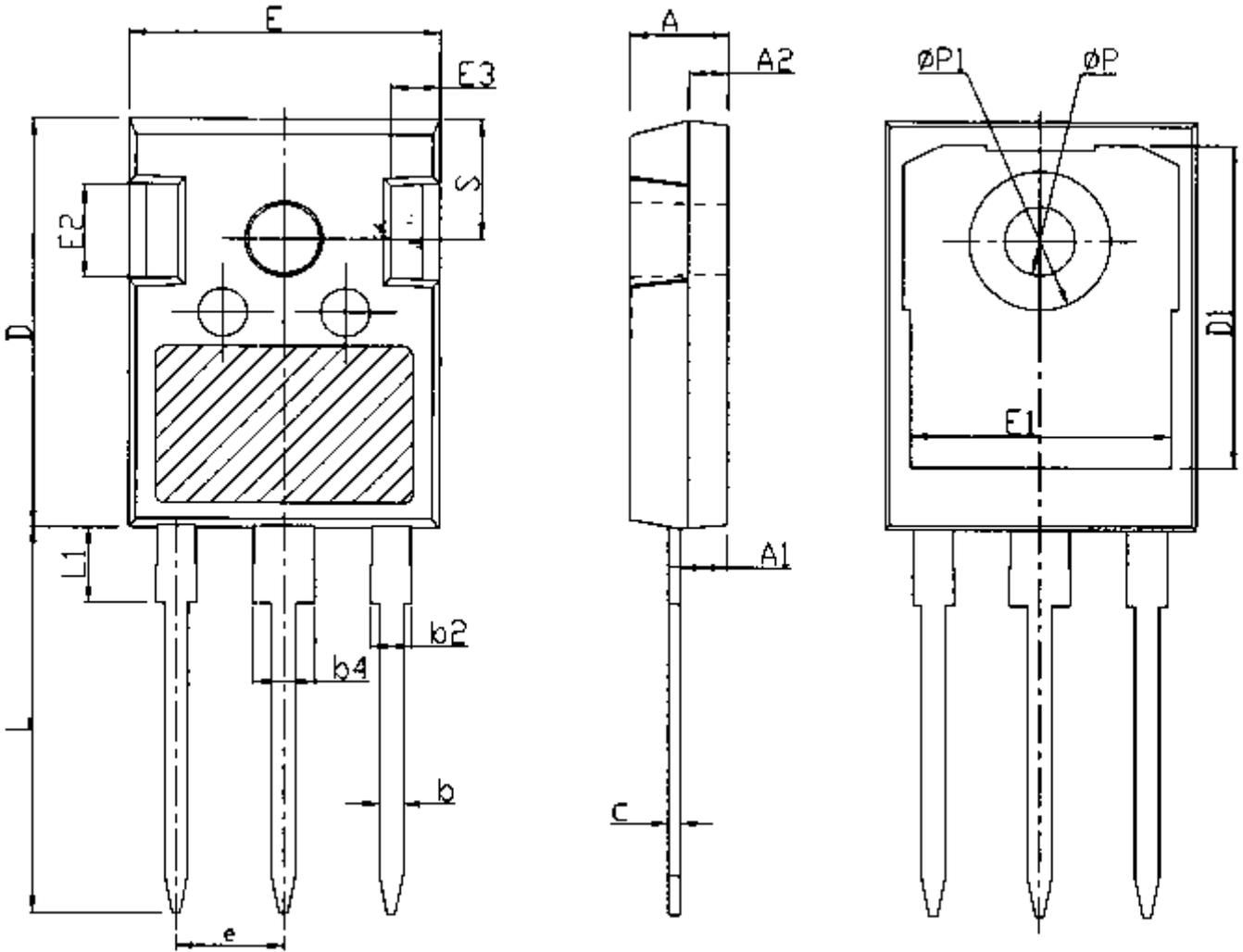


Figure C: Unclamped Inductive Switching Test Circuit and Waveform





### TO-247



Unit:mm			
Symbol	Min.	Nom	Max.
A	4.80	5.00	5.20
A1	2.21	2.41	2.61
A2	1.85	2.00	2.15
b	1.11	1.21	1.36
b2	1.91	2.01	2.21
b4	2.91	3.01	3.21
c	0.51	0.61	0.75
D	20.70	21.00	21.30
D1	16.25	16.55	16.85

Unit:mm			
Symbol	Min.	Nom.	Max.
E	15.50	15.80	16.10
E1	13.00	13.30	13.60
E2	4.80	5.00	5.20
E3	2.30	2.50	2.70
e	5.44BSC		
L	19.62	19.92	20.22
L1	-	-	4.30
$\phi P$	3.40	3.60	3.80
$\phi P1$	-	-	7.30
S	6.15BSC		



## Disclaimer

All product specifications and data are subject to change without notice.

For documents and material available from this datasheet, Wuxi Unigroup does not warrant or assume any legal liability or responsibility for the accuracy, completeness of any product or technology disclosed hereunder.

No license, express or implied, by estoppels or otherwise, to any intellectual property rights is granted by this document or by any conduct of Wuxi Unigroup.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling Wuxi Unigroup products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Wuxi Unigroup for any damages arising or resulting from such use or sale.

Wuxi Unigroup disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Wuxi Unigroup's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

Wuxi Unigroup Microelectronics CO., LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

In the event that any or all Wuxi Unigroup products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

Information (including circuit diagrams and circuit parameters) herein is for example only. It is not guaranteed for volume production. Wuxi Unigroup believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.